

FIG. 1

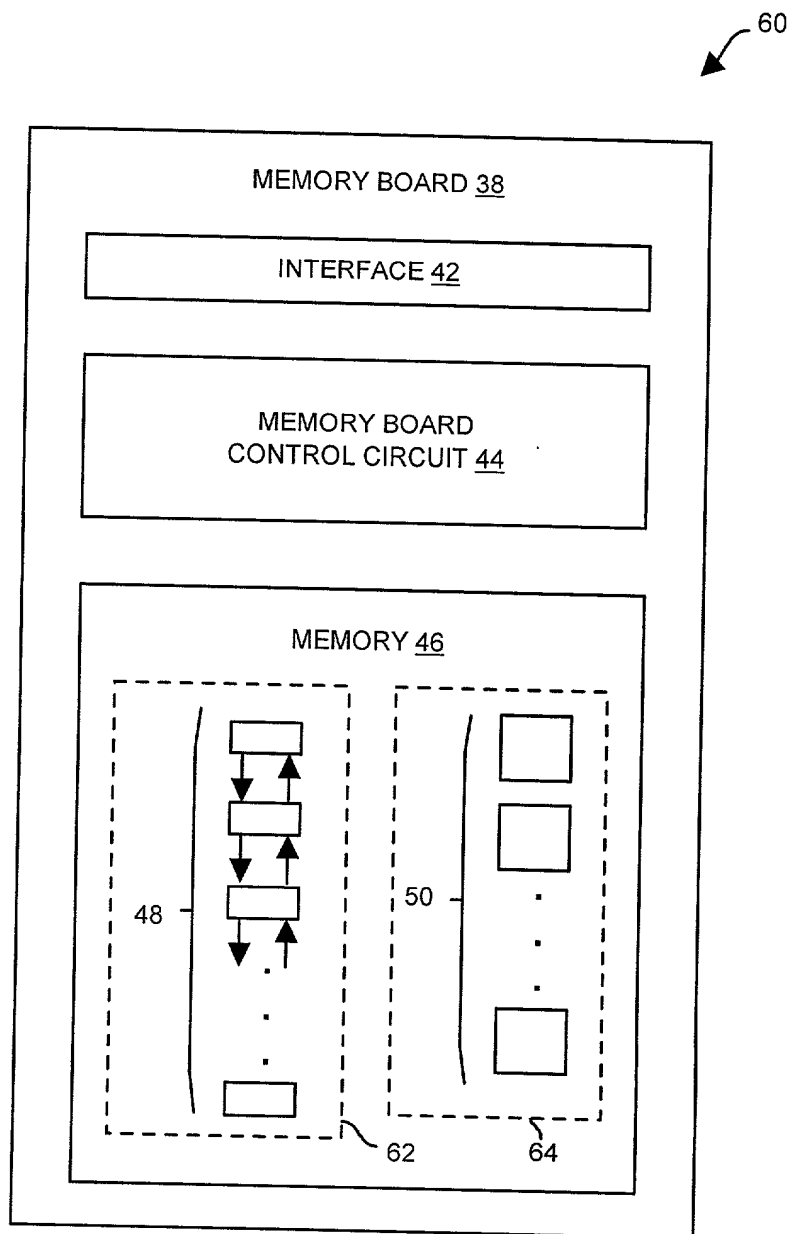


FIG. 2

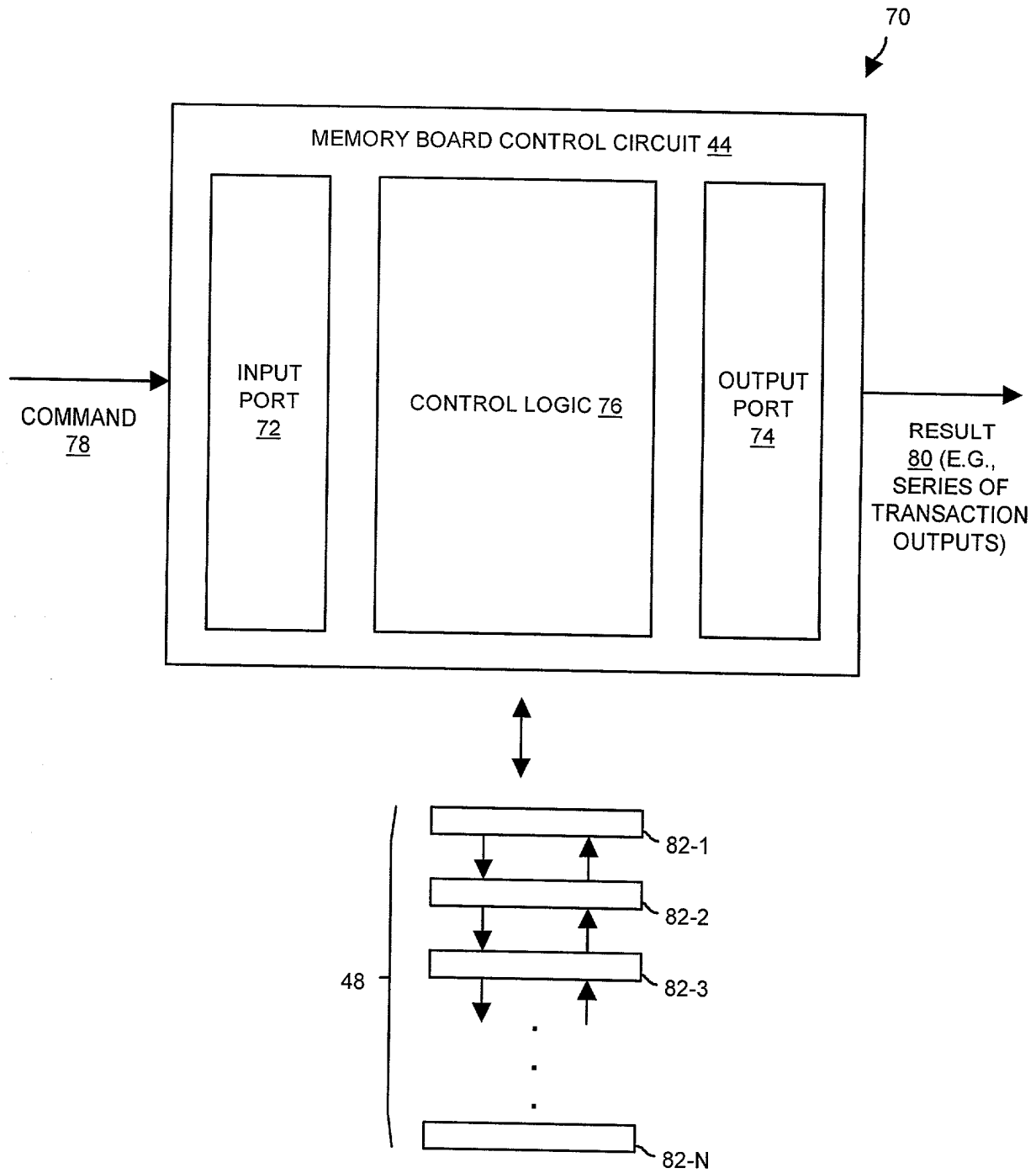


FIG. 3

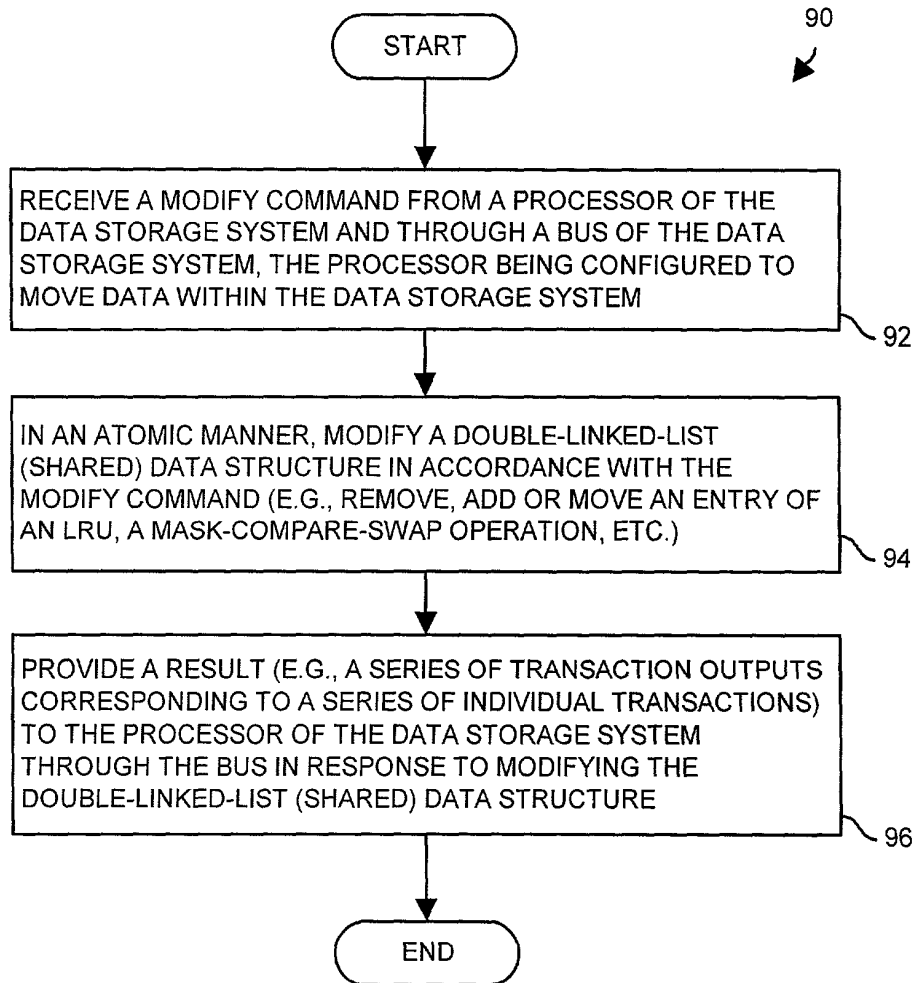


FIG. 4

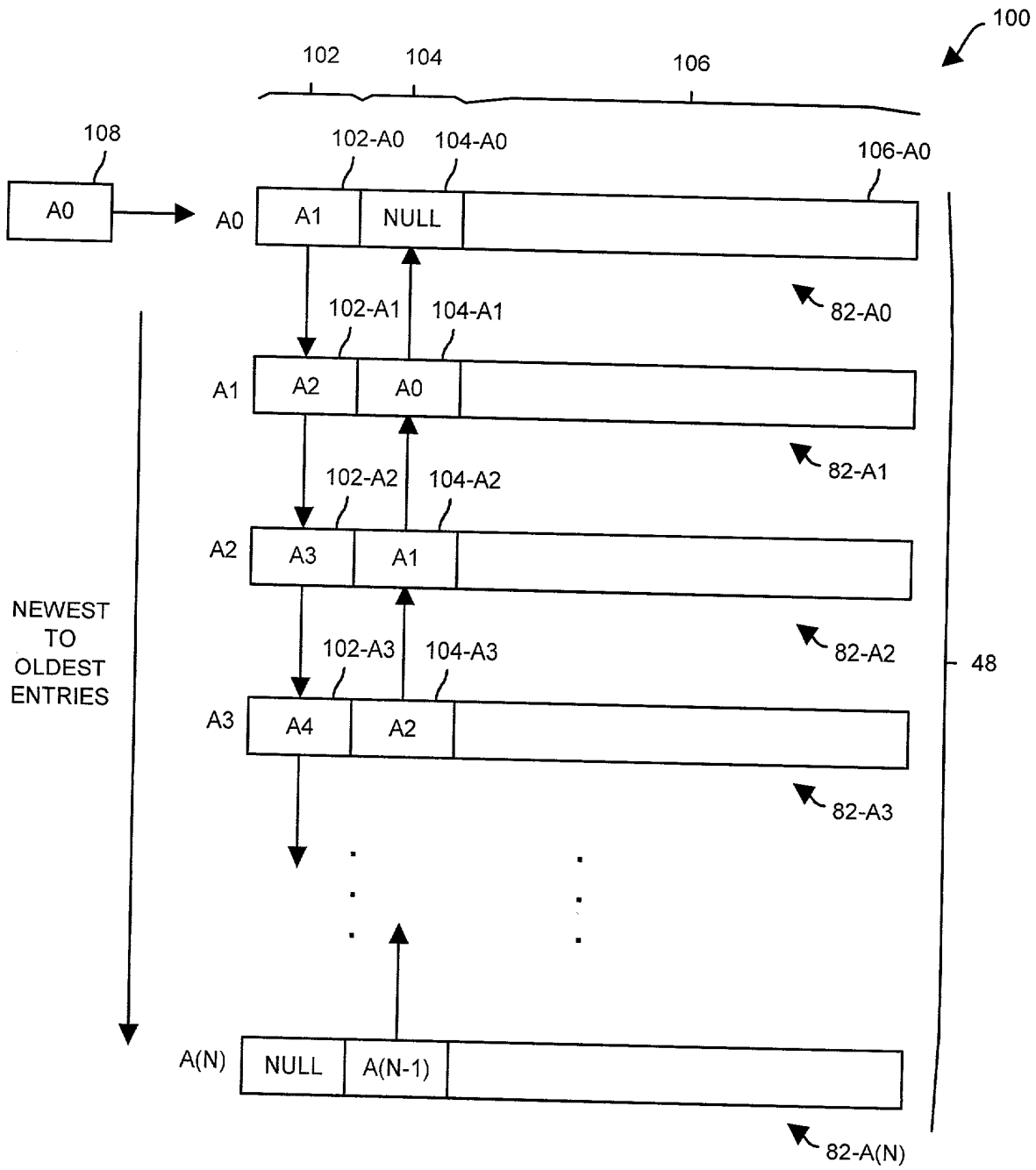


FIG. 5

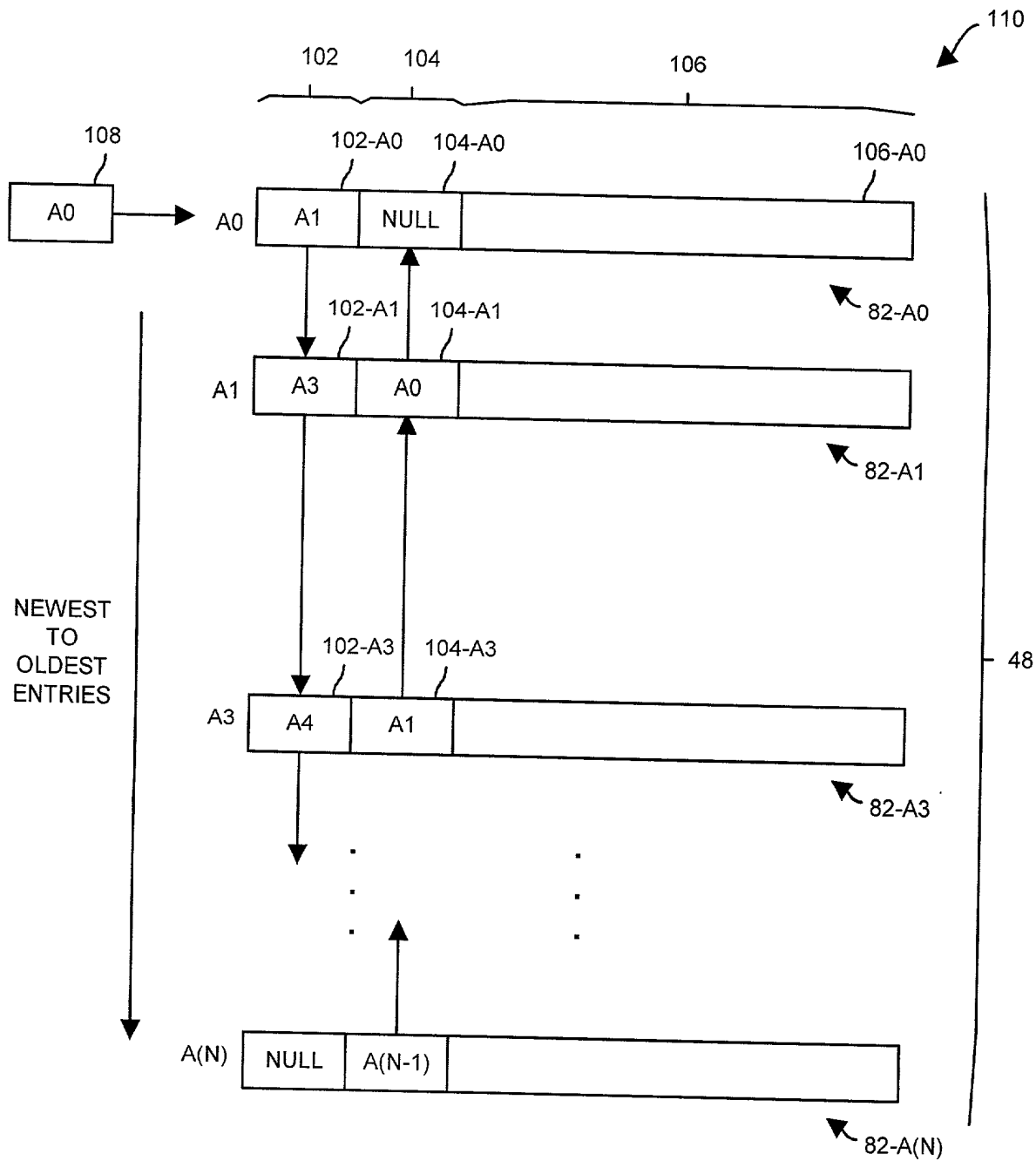


FIG. 6

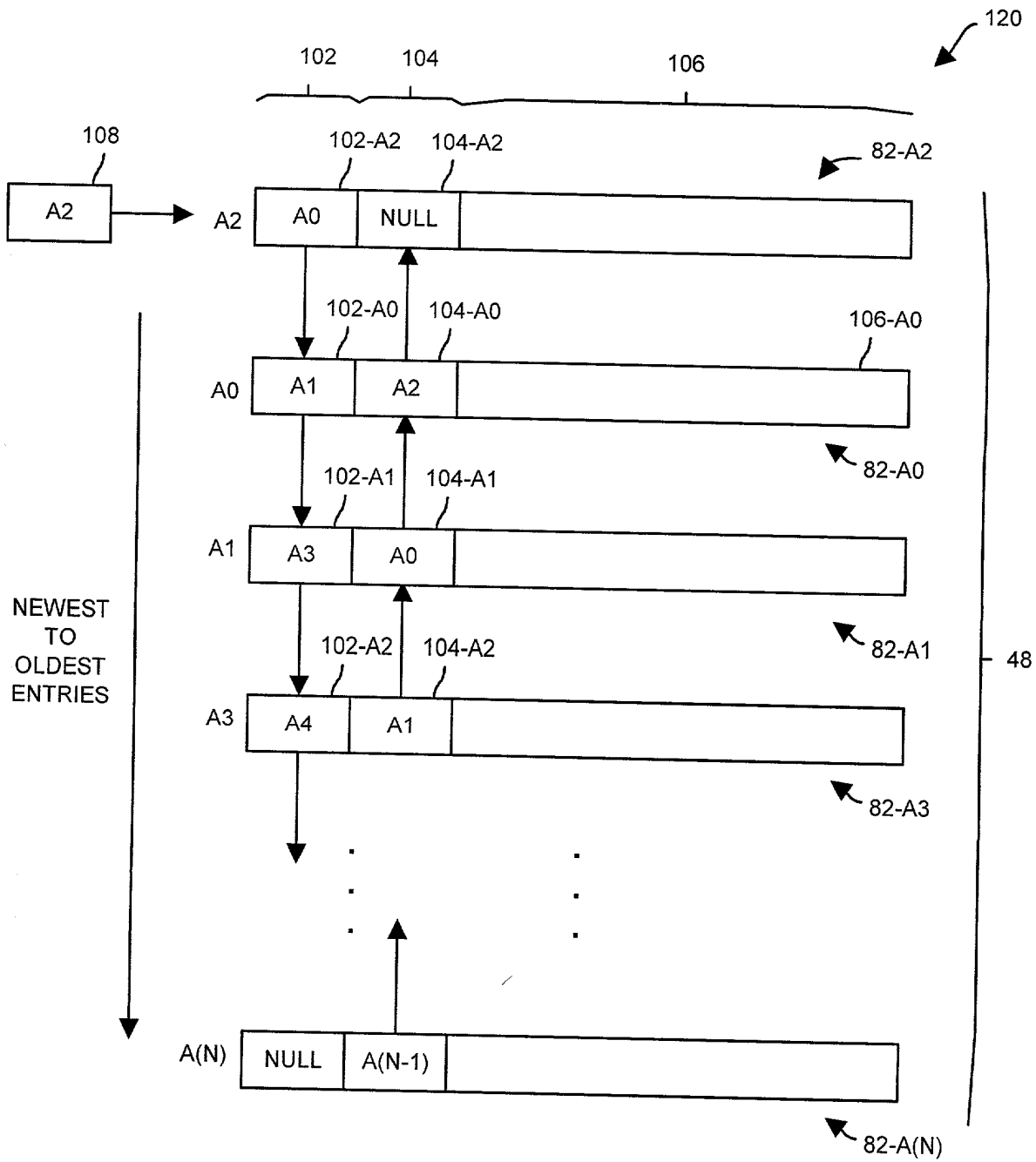


FIG. 7

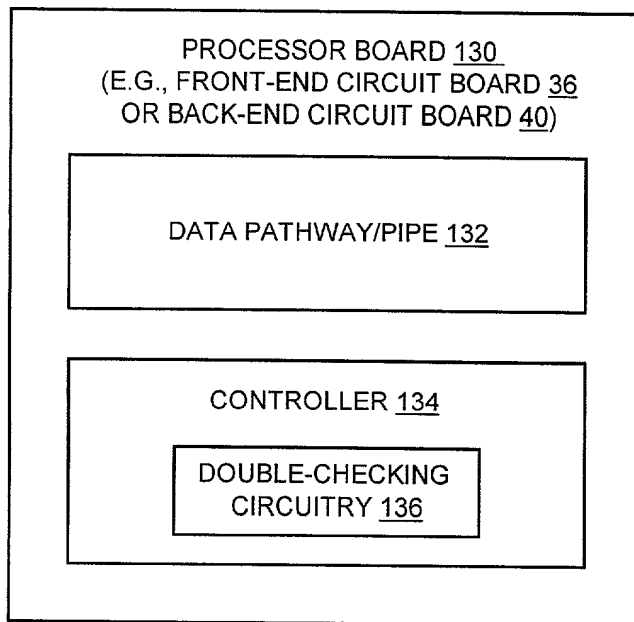


FIG. 8



PROCESSOR  
BOARD 130  
(E.G., FRONT-END  
DIRECTOR)

ONE OF THE SET OF BUSES 32

CONTROL LOGIC 76  
OF MEMORY  
BOARD 38

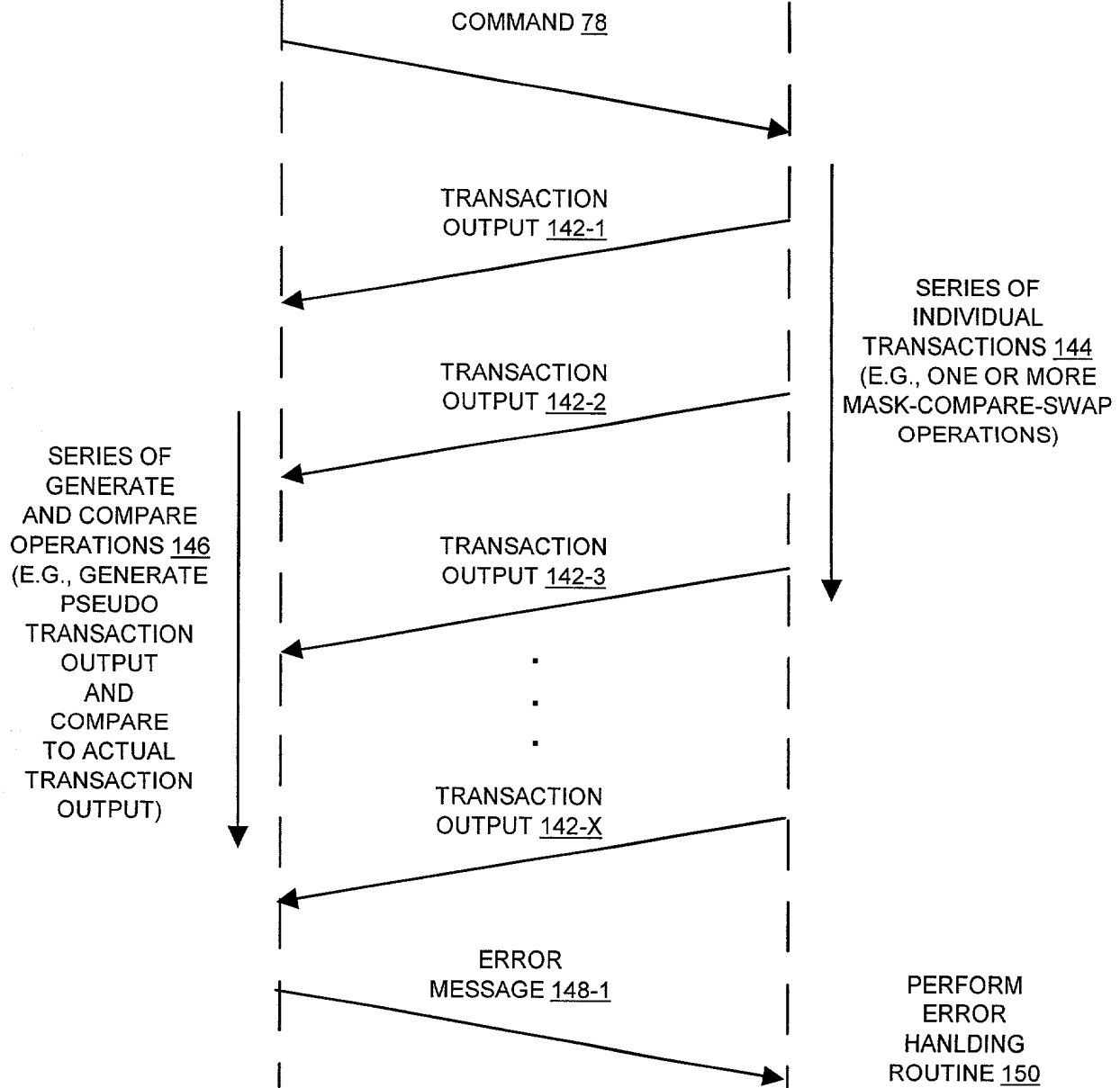


FIG. 9